09/919,372

L Number	Hits	Search Text	DB	Time stamp
1	2304257	phase detectors	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:15
2	3635313	clock circuits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:16
3	4685172	loop filter circuits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:16
4	3588870	oscillator circuits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:16
5	798683	(phase detectors) and (clock circuits)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:18
6	3498903	(clock circuits) and (oscillator circuits)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:29
7	770323	((phase detectors) and (clock circuits)) and ((clock circuits) and (oscillator circuits))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:37
8	3509517	summation circuits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:37
9	4500058	composite error signals	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:37
10	1858875	(summation circuits) and (composite error signals)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:40
11	594194	(((phase detectors) and (clock circuits)) and ((clock circuits) and (oscillator circuits))) and ((summation circuits) and (composite error signals))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:45
12	594173	(loop filter circuits) and ((((phase detectors) and (clock circuits)) and ((clock circuits) and (oscillator circuits))) and ((summation circuits) and (composite error signals)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:52

13	3831222	clock signals	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:53
14	7064572	control signals	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:53
15	3772038	(clock signals) and (control signals)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 08:59
16	4292781	pulse generator circuits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 09:04
17	572901	((loop filter circuits) and ((((phase detectors) and (clock circuits)) and ((clock circuits) and (oscillator circuits))) and ((summation circuits) and (composite error signals)))) and ((clock signals) and (control signals))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 09:09
18	572881	(pulse generator circuits) and (((loop filter circuits) and ((((phase detectors) and (clock circuits)) and ((clock circuits) and (oscillator circuits))) and ((summation circuits) and (composite error signals)))) and ((clock signals) and (control signals)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 09:20
19	225293	synchroniz$3 and (phase$1 with clock signal$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 09:20
20	95328	((pulse generator circuits) and (((loop filter circuits) and ((((phase detectors) and (clock circuits)) and ((clock circuits) and (oscillator circuits))) and ((summation circuits) and (composite error signals)))) and ((clock signals) and (control signals)))) and (synchroniz$3 and (phase$1 with clock signal$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 09:23
21	3373415	single integrated chip	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 09:24
22	66885	(((pulse generator circuits) and (((loop filter circuits) and ((((phase detectors) and (clock circuits)) and ((clock circuits) and (oscillator circuits))) and ((summation circuits) and (composite error signals)))) and ((clock signals) and (control signals)))) and (synchroniz$3 and (phase$1 with clock signal$1))) and (single integrated chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 09:26
23	5250	714/789.ccls. or 714/700.ccls. or 714/699.ccls. or 714/7738.ccls. or 714/744.ccls. or 714/814.ccls. or 714/815.ccls. or 327/?.ccls. or 713/?.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 09:27
24	512	(((((pulse generator circuits) and (((loop filter circuits) and ((((phase detectors) and (clock circuits)) and ((clock circuits) and (oscillator circuits))) and ((summation circuits) and (composite error signals)))) and ((clock signals) and (control signals)))) and (synchroniz$3 and (phase$1 with clock signal$1))) and (single integrated chip)) and (714/789.ccls. or 714/700.ccls. or 714/699.ccls. or 714/7738.ccls. or 714/744.ccls. or 714/814.ccls. or 714/815.ccls. or 327/?.ccls. or 713/?.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 09:28

| 25 | 98 | ("6066969"
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"5633879"
"5646672"
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| 26 | | 2 | ((((((pulse generator circuits) and (((loop filter circuits) and ((((phase detectors) and (clock circuits)) and ((clock circuits) and (oscillator circuits))) and ((summation circuits) and (composite error signals)))) and ((clock signals) and (control signals)))) and (synchroniz$3 and (phase$1 with clock signal$1))) and (single integrated chip)) and (714/789.ccls. or 714/700.ccls. or 714/699.ccls. or 714/7738.ccls. or 714/744.ccls. or 714/814.ccls. or 714/815.ccls. or 327/?.ccls. or 713/?.ccls.)) and (("6066969" | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/21 09:29 |
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